25.   (original) The method of claim 22, further comprising changing the root mean square (RMS) noise at an output of the amplifier in response to changing the GBW.

26.   (original) The method of claim 25, wherein said changing the RMS noise comprises:

decreasing the RMS noise in response to a decrease in the GBW; and

increasing the RMS noise in response to an increase in the GBW.

27.   (new) The method of claim 8, further comprising applying a minimum amount of bias current to all transistors for preventing the transistors from turning off.

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28.
27.   (new) The method of claim 8, further comprising controlling each set of two parallel transistors by a respective terminal tail bias.